30V N-CHANNEL ENHANCEMENT MODE MOSFET

SUMMARY

 $V_{(BR)DSS}$ = 30V; $R_{DS(ON)}$ = 0.050 Ω I_D = 4.6A

DESCRIPTION

This new generation of TRENCH MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.



SOT23-6

FEATURES

- Low on-resistance
- · Fast switching speed
- · Low threshold
- · Low gate drive
- SOT23-6 package

APPLICATIONS

- DC DC Converters
- Power Management Functions
- Disconnect switches
- Motor control

ORDERING INFORMATION

DEVICE	REEL SIZE	TAPE WIDTH	QUANTITY PER REEL
ZXMN3A03E6TA	7″	8mm	3000 units
ZXMN3A03E6TC	13"	8mm	10000 units

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DEVICE MARKING

• 3A3



ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V _{DSS}	30	V
Gate Source Voltage	V _{GS}	±20	V
Continuous Drain Current $V_{GS}=10V$; $T_A=25^{\circ}C$ (b) $V_{GS}=10V$; $T_A=70^{\circ}C$ (b) $V_{GS}=10V$; $T_A=25^{\circ}C$ (a)	ID	4.6 3.7 3.7	А
Pulsed Drain Current (c)	I _{DM}	17	Α
Continuous Source Current (Body Diode) (b)	IS	2.6	А
Pulsed Source Current (Body Diode) (c)	I _{SM}	17	Α
Power Dissipation at T _A =25°C (a) Linear Derating Factor	PD	1.1 8.8	W mW/°C
Power Dissipation at T _A =25°C (b) Linear Derating Factor	PD	1.7 13.6	W mW/°C
Operating and Storage Temperature Range	T _j :T _{stg}	-55 to +150	°C

THERMAL RESISTANCE

PARAMETER	SYMBOL	VALUE	UNIT
Junction to Ambient (a)	$R_{\theta JA}$	113	°C/W
Junction to Ambient (b)	$R_{\theta JA}$	73	°C/W

NOTES

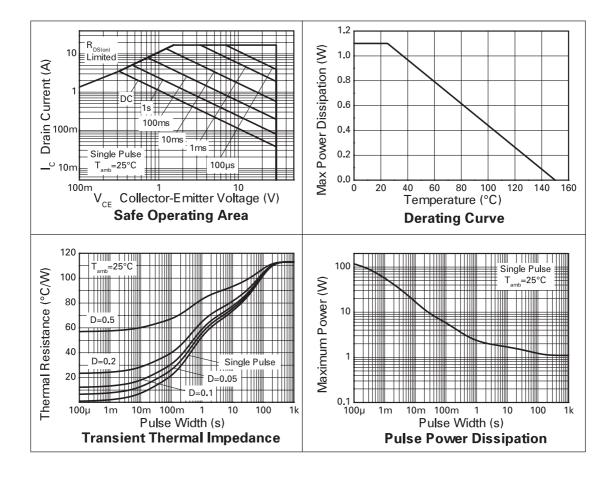
(a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions

(b) For a device surface mounted on FR4 PCB measured at t≤10 secs.



⁽c) Repetitive rating 25mm x 25mm FR4 PCB, D = 0.05, pulse width $10\mu s$ - pulse width limited by maximum junction temperature. Refer to Transient Thermal Impedance graph.

CHARACTERISTICS





ELECTRICAL CHARACTERISTICS (at $T_A = 25^{\circ}C$ unless otherwise stated).

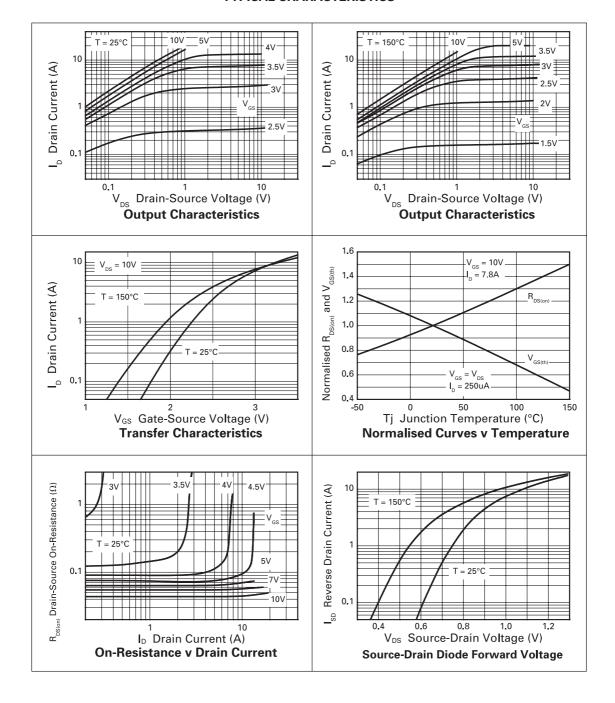
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS.	
STATIC							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	30			V	I _D =250μA, V _{GS} =0V	
Zero Gate Voltage Drain Current	I _{DSS}			0.5	μΑ	V _{DS} =30V, V _{GS} =0V	
Gate-Body Leakage	I _{GSS}			100	nA	V _{GS} =±20V, V _{DS} =0V	
Gate-Source Threshold Voltage	VGS(th)	1			V	I _D =250μA, V _{DS} = V _{GS}	
Static Drain-Source On-State Resistance (1)	R _{DS(on)}			0.050 0.065	Ω Ω	V _{GS} =10V, I _D =7.8A V _{GS} =4.5V, I _D =6.8A	
Forward Transconductance (1)(3)	9fs		10		S	V _{DS} =10V,I _D =7.8A	
DYNAMIC (3)							
Input Capacitance	Ciss		600		pF		
Output Capacitance	Coss		104		pF	V _{DS} =25 V, V _{GS} =0V, f=1MHz	
Reverse Transfer Capacitance	C _{rss}		58.5		pF]	
SWITCHING(2) (3)			•				
Turn-On Delay Time	td(on)		2.9		ns		
Rise Time	t _r		6.4		ns	V _{DD} =15V, I _D =3.5A	
Turn-Off Delay Time	td(off)		16.0		ns	$R_{G}=6.0\Omega$, $V_{GS}=10V$	
Fall Time	t _f		11.2		ns	1	
Gate Charge	Qg		6.9		nC	V _{DS} =15V,V _{GS} =5V, I _D =3.5A	
Total Gate Charge	Qg		12.6		nC	V _{DS} =15V,V _{GS} =10V, I _D =3.5A	
Gate-Source Charge	Qgs		2.0		nC		
Gate-Drain Charge	Q _{gd}		2.0		nC		
SOURCE-DRAIN DIODE							
Diode Forward Voltage (1)	V _{SD}		0.85	0.95	V	TJ=25°C, IS=3.2A, VGS=0V	
Reverse Recovery Time (3)	t _{rr}		18.8		ns	T _J =25°C, I _F =3.5A,	
Reverse Recovery Charge (3)	Q _{rr}		14.1		nC	di/dt= 100Å/μs	

NOTES

- (1) Measured under pulsed conditions. Width=300 $\mu s.$ Duty cycle $\leq~2\%$.
- (2) Switching characteristics are independent of operating junction temperature.
- (3) For design aid only, not subject to production testing.



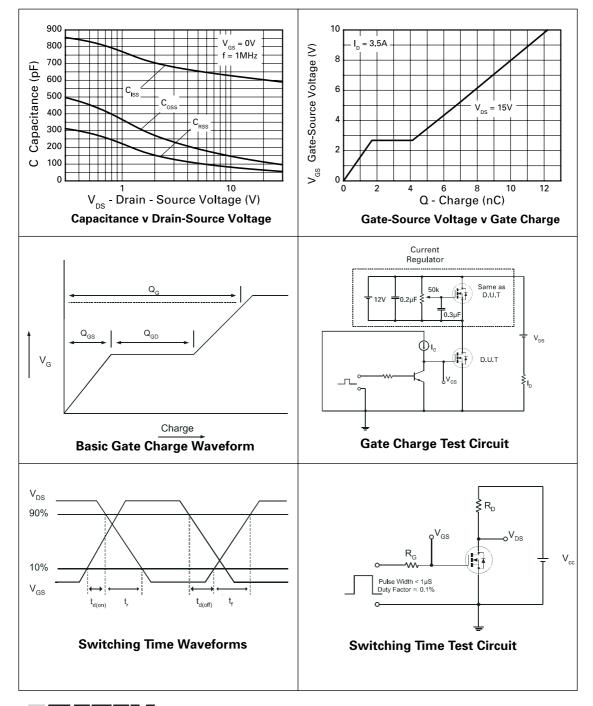
TYPICAL CHARACTERISTICS



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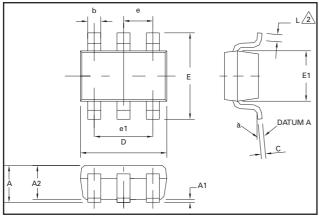


TYPICAL CHARACTERISTICS



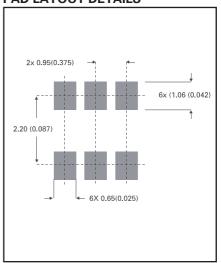


PACKAGE DIMENSIONS



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DIM	Millimetres		Inches		
	Min	Max	Min	Max	
Α	0.90	1.45	0.35	0.057	
A1	0.00	0.15	0	0.006	
A2	0.90	1.30	0.035	0.051	
b	0.35	0.50	0.014	0.019	
С	0.09	0.20	0.0035	0.008	
D	2.80	3.00	0.110	0.118	
Е	2.60	3.00	0.102	0.118	
E1	1.50	1.75	0.059	0.069	
L	0.10	0.60	0.004	0.002	

PAD LAYOUT DETAILS



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0.95 REF

1.90 REF

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